

FIG. 1 (PRIOR ART)

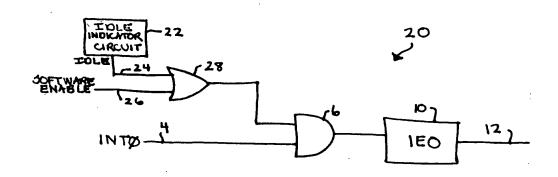


FIG. 2

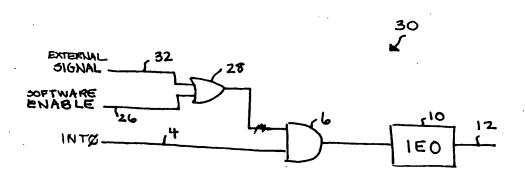


FIG. 3

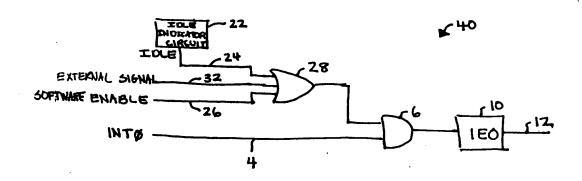


FIG 4

FIG. 5a

NCLK XTAL

NCLK XTAL

RTC XTAL

RTC XTAL

FROM FIG. 50

FIG. 5b

0.1.2.1.19 0.1.2.2.19 1.6.5.4.3.0.19

CPUCLKOUT, CBOUT2,

ROM